

# DA at NIMH - experience, plans

Andrey Bogatchev, Lisbon 22 March  
2017

Current status of NWP at NIMH:

The biggest new is the new machine – it is computational server delivered during the last quarter of 2016 with following characteristics:

Barebone Supermicro  
Superserver 8028B-C0R3FT  
Four processors Intel Xeon E7-  
4850v.3, 2.2 GHz, 35 MB cache,  
14 core, w/HT  
512 GB memory  
36 TB disk storage under RAID  
controler  
CEntOS 7.2  
Intel Fortran and C++ parallel  
compilers

## E-suites:

1. ALADIN BG 7/105

LONC=25.5,LATC=42.75

NDLON = 180, NDGLG = 144

NDLUX=169, NDGUX=133

NMSMAX=89, NSMAX= 71

NFLEV=105,EDELX=7000

2. ALADIN BG 5/105

LONC=25.5,LATC=42.75

NDLON=256, NDGLG=200

NDLUX=245 NDGUX=189

NMSMAX=127, NSMAX=99

NFLEV=105,EDELX=5000

3. AROME BG 2.5/60  
LONC=25.5,LATC=42.75  
NDLON=320, NDGLG=240  
NDLUX=309, NDGUX=229  
NMSMAX=159, NSMAX=119  
NFLEV=50, EDELX=2500

## Plans:

1. ALADIN BG 5/105 to become operational to the First of July 2017 with two runs per day 06 and 18 UTC
2. AROME BG to become operational to the First of July 2017 with two runs per day 06 and 18 UTC
3. Switch to 4 runs per day of ALADIN and AROME October 2017

## Data Assimilation:

1. Experience – none

2. Plans:

- start preparations for building of surface DA for ALADIN – September 2017.
- experiments with surface DA November 2017
- start of operational surface DA for ALADIN not later than March 2018
- start preparation of Surface DA for AROME

Thank you!  
Questions, remarks,  
comments

